37th International Symposium on Multiple-Valued Logic

Oslo, Norway,   May 13 - 16, 2007

CALL FOR PAPERS

The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 37th annual symposium on May 13-16, 2007 in Oslo, Norway. The event is sponsored by the IEEE Computer Society and the University of Oslo. You are invited to submit an original paper, survey or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

- **Algebraic and Formal Aspects**
- **Automatic Reasoning**
- **Communication Systems**
- **Data Mining**
- **Image Processing**
- **Logic Programming**
- **Nano Technology**
- **Quantum Computing**
- **Spectral Techniques**

- **ATPG and SAT**
- **Circuit/Device Implementation**
- **Computer Arithmetic**
- **Fuzzy Logic and Soft Computing**
- **Logic Design and Switching Theory**
- **Machine Learning and Robotics**
- **Philosophical Aspects**
- **Signal Processing**

Authors should submit papers by **November 1, 2006** to:

Prof. Dr.-Ing. habil. Bernd Steinbach, ISMVL-2007 Program Chair
TU Bergakademie Freiberg, Fakultät für Mathematik und Informatik, Institut für Informatik,
Bernhard-von-Cotta-Straße 2 09596 Freiberg,   Tel.: +49 (3731) 39 25 68   E-Mail: steinb@informatik.tu-freiberg.de

Electronic submissions by PDF or PS files are strongly preferred. In exceptional cases, submissions of four (double-spaced typed) paper copies are also acceptable. Each manuscript should include a 50-100 word abstract and should not exceed 6 pages in the proceedings format.

Authors will be notified about acceptance by February 1, 2007, photo-ready copies of accepted papers are due on March 1, 2007.

The ISMVL-2007 Symposium Chair: Prof. Yngvar Berg, Dept. of Informatics, University of Oslo, NORWAY
For additional information contact: ismvl2007@ifi.uio.no or visit www.ismvl.org.

The symposium will be preceded by the Post Binary ULSI Workshop on May 13, 2007